



# Digital Logic Design: Tutorial and Laboratory Exercises

*John Passafiume, Michael Douglas*

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The perfect complement to computer architecture and logic texts. This widely praised tutorial and lab book gives practice in the fundamentals of digital logic and circuitry, with special emphasis on how the machine operates at the gate and register level. Presentation employs the TTL family of digital logic due to its wide availability and moderate cost. Exercises require the student to perform a simple designs and then implement them on hardware. Contains sufficient exercises for a 3-hour lab meeting, once a week, for a semester.

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