



ISA and EISA Theory and Operation

Edward Solari



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This reference provides authoritative and detailed timing specifications for the ISA and EISA buses. Provided are hundreds of diagrams and tables that contain all the timing information relative to both system board and add-on card design. The specifications covered are consistent with, and improve upon, the IEEE P996 specs for the AT bus and the Rev 3.12 specification for the EISA bus.

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